



PATENT  
P56937

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

JAE-BON KOO *et al.*

Serial No.: 10/764,525

Examiner: LUKE, DANIEL M.

Filed: 27 January 2004

Art Unit: 2813

For: FLAT PANEL DISPLAY WITH ANODE ELECTRODE LAYER AS POWER SUPPLY LAYER AND FABRICATION METHOD THEREOF

**AMENDMENT & RESPONSE UNDER 37 C.F.R. §1.143 TO**  
**A RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Entry of Applicant's response to the restriction requirement set forth in the Office action mailed on 1 April 2008 (Paper No. 20080325), reconsideration and re-examination are respectfully requested.

**CERTIFICATE OF**  
**FACSIMILE TRANSMISSION**

I hereby certify that, on 1 May 2008, this correspondence is being facsimile transmitted to the U.S. Patent & Trademark Office (Facsimile No. 571-273-8300)

Total 13 sheets

*J. Watson*

For Robert E. Bushnell  
Reg. No. 27,774

Folio: P56937  
Date: 5/1/08  
I.D.: REB/fw